

EAST

File View Edit Tools Window Help

09423415RCE.wsp:1

☐ Drafts
☐ Pending
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☐ Failed
☐ Saved
☐ Favorites
☐ Tagged (7)

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DBs: US:PGPUB:USPAT:EPO:JPO:DERWENT:IBM:TDB

Default operator: OR

☐ Plurals
☒ Highlight all hit terms initially

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5760783 A	19980602	26	Method and system for providing texture using a	345/587	
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5768445 A	19980616	13	Compression and decompression scheme perf	382/305	382/232
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5790130 A	19980804	77	Texel cache interrupt daemon for virtual memory	345/587	
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5936632 A	19990810	12	Method for fast downloading of textures t	345/582	345/619
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6166748 A	20001226	140	Interface for a high performance low cost vide	345/522	
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6353438 B1	20020305	27	Cache organization--direct mapp	345/552	345/540; 345/556;
7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6417860 B1	20020709	27	Method and system for providing texture using a	345/582	345/426; 345/427;

Start [Icons] 9:14 AM

EAST

File View Edit Tools Window Help

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Drafts

Pending

Active

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Go

Save

Print

Close

DBs

US-PGPUB;USPAT;EPO;JPO;DERWENT;IBM_TDB

Plurals

Default operator

OR

Highlight all hit terms initially

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRe	F
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5911051 A	19990608	72	High-throughput interconnect allowing bus	710/107		
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5974471 A	19991026	10	Computer system having distributed compression a	710/1	709/247	
3	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 5977997 A	19991102	11	Single chip computer having integrated MPEG an	345/519	345/503;	
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6130680 A	20001010	21	Method and apparatus for multi-level demand cachin	345/544	345/503;	
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6249853 B1	20010619	25	GART and PTES defined by configuration registers	711/206	345/538;	
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6353438 B1	20020305	27	Cache organization--direct mapp	345/552	345/540;	
7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6667744 B2	20031223	35	High speed video frame buffer	345/502	345/556;	
8	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5974471 A	19991026	10	Codec logic for data transfer in computer syst		345/545;	
9	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6249853 B	20010619	25	Graphical address remapping table for compu			
10	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6353438 B	20020305	27	Accessing method for tag information in memory use			

Start

9:12 AM